



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : Confirmation No. 3641
Hiroshi YAMAUCHI et al. : Docket No. 2001_0929A
Serial No. 09/892,746 : Group Art Unit 2827
Filed June 28, 2001 : Examiner J. Vigushin

AGGREGATE OF ELECTRONIC COMPONENTS
AND MOUNTING METHOD THEREOF

THE COMMISSIONER IS AUTHORIZED
TO CHARGE ANY DEFICIENCY IN THE
FEES FOR THIS PAPER TO DEPOSIT
ACCOUNT NO. 23-0975

#7 Election
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RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents,
Washington, D.C.

Sir:

In response to the Restriction Requirement of September 30, 2002, Applicants hereby elect **Group II**, which is drawn to an aggregate of electronic components and is embodied by claims 1-4 and 6-12.

Having made the required election, a full examination on the merits of the elected group is requested.

Respectfully submitted,

Hiroshi YAMAUCHI et al.

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